

# Various Useful Modules

## VME Slave Interface Extension (FEA\_VME\_P2)

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- 1 GENERAL REMARKS
- 2 LOGICAL DATA
- 3 MECHANICAL DATA

## 1 General Remarks

The module is 54 x 36 mm (2150 x 1400 mil) mezzanine board.

Power consumption: ~ ? A @ 5 V.

The module contains the logic and all the bus transceivers necessary to extend the VME slave interface of the module FEA\_VME\_P1 to address modes up to **A64** and data transfers up to **D64**. It handles all VME bus signals on the P2 connector.

The module is based on the CYPRESS chips [CY7C964](#). For details see the datasheets and application notes.

## 2 Logical Data

The symbol to be used on schematic drawings in the DESY-FE Mentor System is:

**FEA\_VME\_P2'** (DIGITAL LIB / MISC IC LIB)

The module uses the following pinning:

PIN	SIGNAL		PIN	SIGNAL		PIN	SIGNAL		PIN	SIGNAL
1	LA16		21	LD20		41	VME_A24		61	VME_D28
2	LA17		22	LD21		42	VME_A25		62	VME_D29
3	LA18		23	LD22		43	VME_A26		63	VME_D30
4	LA19		24	LD23		44	VME_A27		64	VME_D31
5	LA20		25	LD24		45	VME_A28		65	VCOMP2*
6	LA21		26	LD25		46	VME_A29		66	VCC
7	LA22		27	LD26		47	VME_A30		67	D64
8	LA23		28	LD27		48	VME_A31		68	VCOMP3*
9	LA24		29	LD28		49	VME_D16		69	Strobe*
10	LA25		30	LD29		50	VME_D17		70	DENO*
11	LA26		31	LD30		51	VME_D18		71	DENIN*
12	LA27		32	LD31		52	VME_D19		72	DENIN1*
13	LA28		33	VME_A23		53	VME_D20		73	LADI
14	LA29		34	VME_A22		54	VME_D21		74	LEDI
15	LA30		35	VME_A21		55	VME_D22		75	LEDO
16	LA31		36	VME_A20		56	VME_D23		76	ABEN*
17	LD16		37	VME_A19		57	VME_D24		77	LDS
18	LD17		38	VME_A18		58	VME_D25		78	LCIN*
19	LD18		39	VME_A17		59	VME_D26		79	VCIN*
20	LD19		40	VME_A16		60	VME_D27		80	GND

### 3 Mechanical Data

The symbol to be used on the PCB in the DESY-FE Mentor System is:

'fea\_vme\_p2' (\$DESY\_GEOM / std\_geom).

